

Fig. 1. The state response of system in Example 1 for discrete delay $h = 0.1$ sec and leakage delay $\tau = 1.1771$ sec.

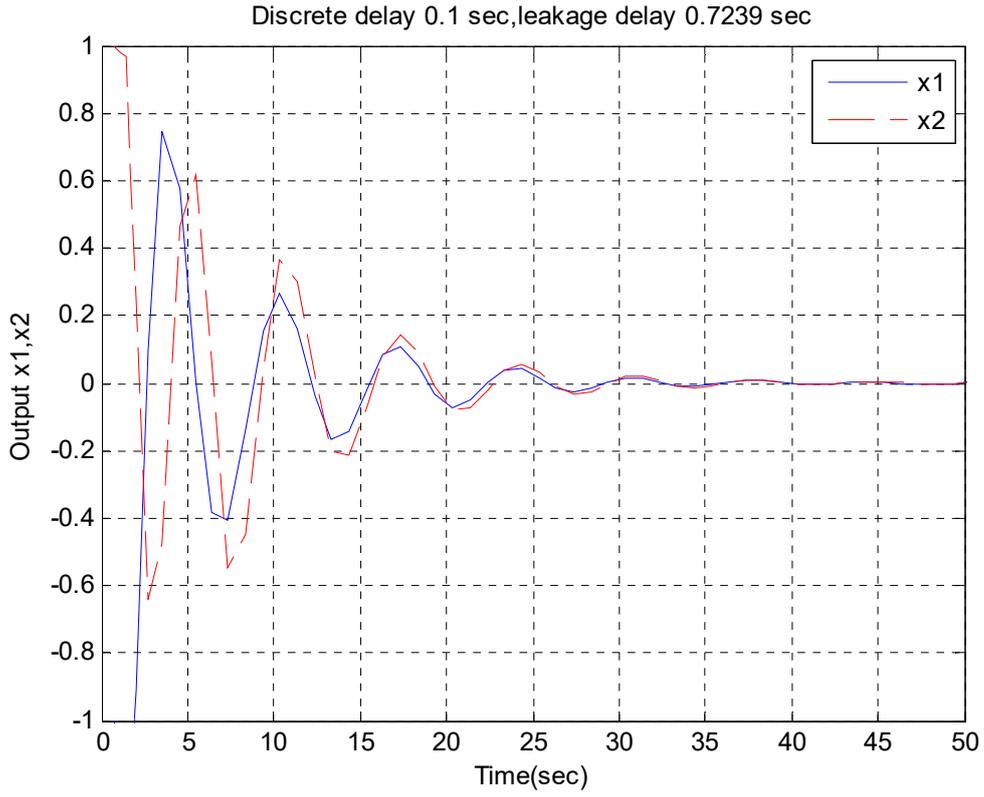


Fig. 2. The state response of system in Example 2 for discrete delay $h_2 = 0.1$ sec and leakage delay $\tau = 0.7329$ sec.

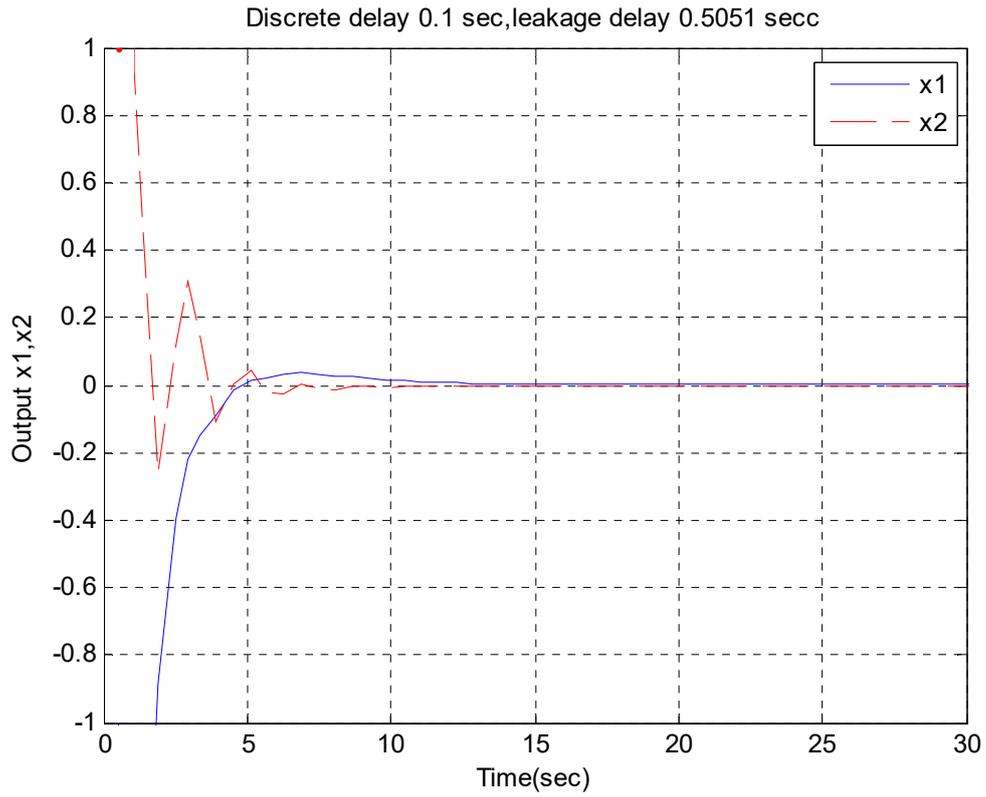


Fig. 3. The state response of system in Example 3 for discrete delay $h_2 = 0.5051$ sec and leakage delay $\tau = 0.1$ sec.

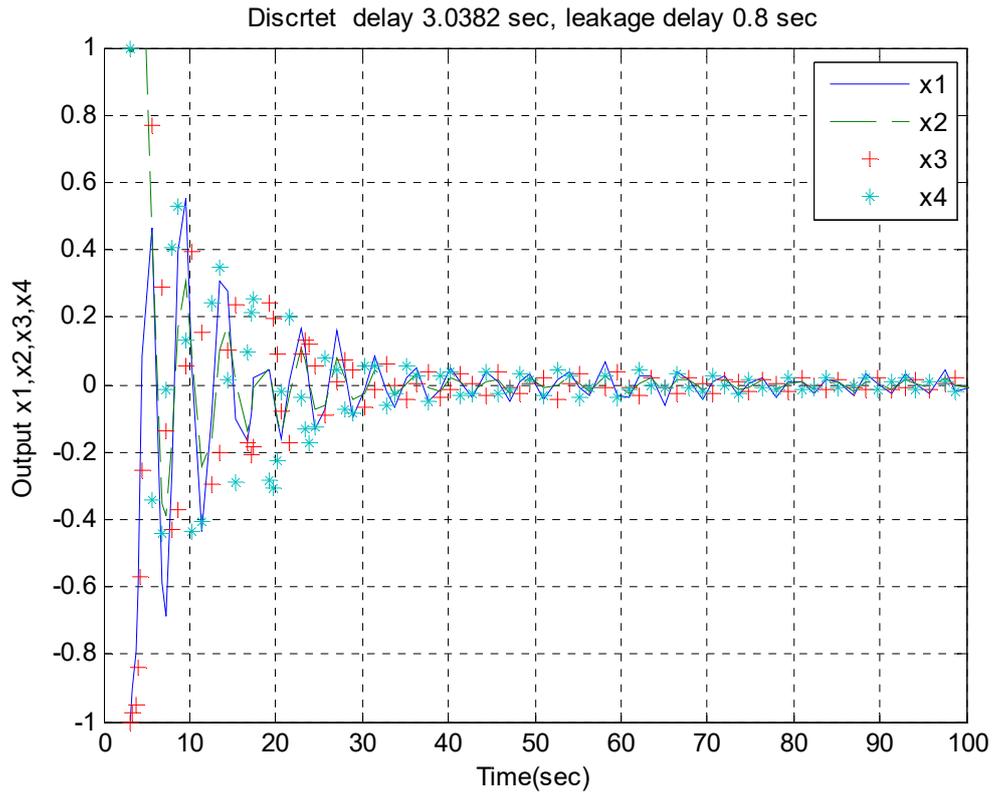


Fig. 4. The state response of system in Example 4 for discrete delay $h_2 = 3.0382$ sec and leakage delay $\tau = 0.8$ sec.